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Attorney's Docket No.: S1022/8719

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: S1022/8719
Serial No.: Unassigned
Filed: Herewith
For: ARITHMETIC UNIT

Examiner: Unassigned
Art Unit: Unassigned

Box Patent Application
Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir/Madam:

Prior to examination please amend the above-identified application as follows:

IN THE SPECIFICATION

Please insert the following paragraphs on page 14, after line 19.

--Having thus described at least one illustrative embodiment of the invention, various alterations, modifications, and improvements will readily occur to those skilled in the art. Such alterations, modifications, and improvements are intended to be within the spirit and scope of the invention. Accordingly, the foregoing description is by way of example only and is not intended as limiting. The invention is limited only as defined in the following claims and the equivalents thereto.

What is claimed is:--

IN THE ABSTRACT

Please amend the abstract as follows.

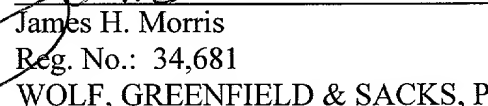
Applicant presents the abstract as amended below and encloses a separate sheet indicating the amendments to the abstract with bracketing and underlining.

An arithmetic unit for adding a plurality of values to define a result, the arithmetic unit including circuitry for receiving the plurality of values; circuitry for adding the plurality of values to define a result, the result being within a first range; circuitry for determining if the result falls within a second range, the second range being smaller than the first range, the circuitry arranged to consider only some of the bits of the result; and circuitry for modifying the result in so that the result output by said arithmetic unit falls within the second range.

REMARKS

This is a Preliminary Amendment in which Applicant has amended the abstract and the specification. No new matter has been added. An early and favorable action is respectfully requested.

Respectfully submitted,


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AMENDED ABSTRACT SHOWING THE AMENDMENTS

An arithmetic unit for adding a plurality of values to define a result, [said] the
5 arithmetic unit [comprising means] including circuitry for receiving [said] the plurality of
values; [means] circuitry for adding [said] the plurality of values to define a result, [said] the
result being within a first range; [means] circuitry for determining if [said] the result falls
within a second range, [said] the second range being smaller than the first range, [said
means] the circuitry arranged to consider only some of the bits of [said] the result; and
10 [means] circuitry for modifying [said] the result in so that the result output by said
arithmetic unit falls within the second range.